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E UNITED STATES PATENT AND TRADEMARK OFFICE

Augulia ation Coriol No.	
Application Serial No	January 5, 2001
Filing Date	January 5, 2001
Inventor	Leonard Forbes, et al.
Assignee	Micron Technology, Inc.
Group Art Unit	2823
Examiner	K.D. Nguyen
Examiner	MIO2 4524
Attornev's Docket No	MI22-1531
Title	Methods of Forming Capacitor Structures

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Reference -See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed after the filing of the Request for Continued Examination (RCE) Application and before receipt of the first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 2/7/00

By:

Robert C. Anyta Reg. No. 46,791 Form PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY, DOCKET NO. MI22-1531

SERIAL NO. 09/755,673

LIST OF ART CITED BY APPLICANT

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APPLICANT: Leonard Forbes, et al.

FILING DATE

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U.S. PATEI	NT DOC	UMENTS		PED S				
Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing E	oriate
	AA	6,203,613 B1	03/01	Gates, et al.				
	АВ	6,509,280 B2	01/03	Choi, Sung-Je				
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